IN THE ABSTRACT

Please replace the Abstract of the Disclosure with the rewritten Abstract of the Disclosure located below:

--In one embodiment of the invention, parameter functions for a plurality of circuits in a subsystem are created. The subsystem has design constraints. Each one of the parameter functions corresponds to each one of the circuits. The parameter functions represent a relationship among design parameters of the subsystem. The design parameters include constraint and optimizing sets. Initial design points are selected on the parameter functions having a first sum of the constraint set and a second sum of the optimizing set such that the first sum satisfies the design constraints. New design points are selected on the parameter functions such that the second sum is improved within the design constraints.--

IN THE CLAIMS

Following is a complete set of claims. No changes have been made to the claims.

CLEAN VERSION OF THE ENTIRE SET OF CLAIMS

1   1.   (THREE TIMES AMENDED) A method comprising:

2   (a) creating parameter functions for a plurality of circuits in a subsystem, the

3   subsystem having design constraints, each one of the parameter functions corresponding to

4   each one of the circuits, the parameter functions representing a relationship among design

5   parameters of the subsystem, the design parameters including constraint and optimizing

6   sets;

7   (b) selecting initial design points on the parameter functions having a first sum of

8   the constraint set and a second sum of the optimizing set such that the first sum satisfies

9   the design constraints; and

10   (c) selecting new design points on the parameter functions such that the second sum

11   is improved within the design constraints.